Rajarshi Shahu Mahavidyalaya(Autonomous), Latur

Department of InformationTechnology.

Academic Year 2023-24

(10 - July - 2023 To 04 - Nov.-2023)

1. Details of Classes to be taught

Sr. No.	Class	Name of Asst. Prof.	Course Title	Course Code	Practical paper code	Total Teaching Hours
1	B.C. A. F.Y. [I - Sem] Div(A)	Mr. Shaikh R.S.S.	Digital Logic	101COA1501	101COA1501	Th-45 PR-15

2. Summary of Lesson Plan

Sr. No	Unit and Chapter to covered	Date from	Date To	No. of lectur ers	Academic activities to be organized	No. of Assignment with topic
1	Unit- I Number System, Complements	10-07-23	25-08-23	15	Class seminars	Assignment and class test
2	Unit- II Basic and Universal Gate, Boolean Algebra	26-08-23	16-09-23	10	Class room practice	Assignment and class test
3	Unit-III Combinational Logic, Multiplexer, De- multiplexer	18-09-23	12-10-23	11	Class room practice	Assignment and class test
4	Unit -IV Sequential Logic, Introduction to Microprocessor	13-10-23	04-11-23	09	Class room practice	Assignment and class test

Course Teacher

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Dept. of Information Technology R.S.M.(Autonomous), Latur Principal
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Rajarshi Shahu Mahavidyalaya,Latur (Autonomous)

Well-Electric

Rajarshi Shahu Mahavidyalaya(Autonomous), Latur

Department of InformationTechnology.

Academic Year 2023-24

(10 - July - 2023 To 04 - Nov.-2023)

1. Details of Classes to be taught

Sr. No.	Class	Name of Asst. Prof.	Course Title	Course Code	Practical paper code	Total Teaching Hours
1	B.C. A. F.Y. [I - Sem] Div(B)	Mr. Shaikh R.S.S.	Digital Logic	101COA1501	101COA1501	Th-45 PR-15

2. Summary of Lesson Plan

Sr. No	Unit and Chapter to covered	Date from	Date To	No. of lectur ers	Academic activities to be organized	No. of Assignment with topic
1	Unit- I Number System, Complements	10-07-23	25-08-23	15	Class seminars	Assignment and class test
2	Unit- II	26-08-23	16-09-23	10	Class room practice	Assignment and class test
3	Unit-III Combinational Logic, Multiplexer, De- multiplexer	18-09-23	12-10-23	11	Class room practice	Assignment and class test
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Academic Year 2023-24

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Sr. No.	Class	Name of Asst. Prof.	Course Title	Course Code	Practical paper code	Total Teaching Hours
1	B.C. A. F.Y. [I - Sem] Div(C)	Mr. Shaikh R.S.S.	Digital Logic	101COA1501	101COA1501	Th-45 PR-15

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4	Unit –IV Sequential Logic, Introduction to Microprocessor	13-10-23	04-11-23	09	Class room practice	Assignment and class test

Course Teacher Shaikh R.S.S.

Dept. of Information Technology R.S.M.(Autonomous), Latur Principal
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Rajarshi Shahu Mahavidyalaya,Latur
(Autonomous)

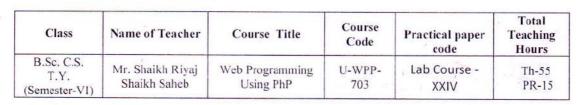
Rajarshi Shahu Mahavidyalaya (Autonomous), Latur

Department of Information Technology

Structured Work Plan for Teaching Academic Year 2023-24

(06-12-2023 TO 29-03-2024)

1. Details of Classes to be taught



2. Summary of Lesson Plan

Sr. No.	Unit and Chapter to be covered	Date From	Date To	No. of Lectures	Academic activities to be organized	No. of Test / Assignment with topic
1	UNIT 1: Introduction to PHP, Handling Html Forms with PHP	06-12-2023	5-1-2024	16	Revision on HTML Concepts	Script/ Programming Test
2	UNIT II: Functions, Arrays.	6-1-2024	17-01-2024	11	Problem Solving Task.	Programming Assignment
3	UNIT III: State management, String matching with Regular Expressions	27-01-2024	20-02-2024	12	Programming Assignment	Question answering Session
4	UNIT IV: OOPS Database connectivity with MYSQL	22-02-2024	29-03-2024	16	Group Discussion	Oral Exam

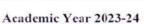
Course Teacher Shaikh Riyaj Shaikh Saheb

Dept. of Information Technology R.S.M.(Autonomous), Latur PRINCIPAL

Rajarshi Shahu Mahavidyalaya,Latur

(Autonomous)

Rajarshi Shahu Mahavidyalaya(Autonomous), Latur Department of Information Technology Structured Work Plan for Teaching



(06-12-2023 TO 29-03-2024)



Class	Name of Teacher	Course Title	Course Code	Practical paper	Total Teaching Hours
B.Sc. C.S. F.Y. Div- A (Semester-II)	Mr. Shaikh Riyaj Shaikh Saheb	Fundamentals of Digital Electronics	101COM2502 (VSC-II)	101COM2502 (VSC-II)	Th-30 PR-20

2. Summary of Lesson Plan

Sr. No.	Unit and Chapter to be covered	Date From	Date To	No. of Lectures	Academic activities to be organized	No. of Test / Assignment with topic
1	UNIT 1: Number System and Codes	08-12-2023	19-01-2024	10	Revision on Fundamental concepts	Question answering Session
2	UNIT II: Logic Gates and Boolean Algebra	27-01-2024	21-02-2024	10	Identification of gates	Building logic circuit
3	UNIT III: Combination and Sequential Logic Circuits	22-02-2024	29-03-2024	10	Seminar on Microprocessor	Assignment on conversion techniques

Course Teacher
Shaikh Rijaj Shaikh Saheb

Dept. of Information Technology R.S.M.(Autonomous), Latur

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Rajarshi Shahu Mahavidyalaya(Autonomous), Latur Department of Information Technology

Structured Work Plan for Teaching

Academic Year 2023-24

(06-12-2023 TO 29-03-2024)

Details of Classes to be taught

Class	Name of Teacher	Course Title	Course Code	Practical paper code	Total Teaching Hours
B.Sc. C.S. F.Y. Div- B (Semester-II)	Mr. Shaikh Riyaj Shaikh Saheb	Fundamentals of Digital Electronics	101COM2502 (VSC-II)	101COM2502 (VSC-II)	Th-30 PR-20

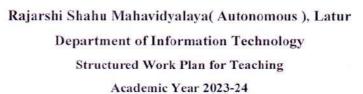
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3	UNIT III: Combination and Sequential Logic Circuits	22-02-2024	29-03-2024	10	Seminar on Microprocessor	Assignment on conversion techniques

Course Teacher Shaikh Riyaj Shaikh saheb

R.S.M.(Autonomous), Latur

Dept. of Information Technology Rajarshi Shahu Mahavidyalaya, Latur (Autonomous)





(06-12-2023 TO 29-03-2024)

Details of Classes to be taught

Class	Name of Teacher	Course Title	Course Code	Practical paper	Total Teaching Hours
B.Sc. C.S. F.Y. Div- C (Semester-II)	Mr. Shaikh Riyaj Shaikh Saheb	Fundamentals of Digital Electronics	101COM2502 (VSC-II)	101COM2502 (VSC-II)	Th-30 PR-20

2. Summary of Lesson Plan

Sr. No.	Unit and Chapter to be covered	Date From	Date To	No. of Lectures	Academic activities to be organized	No. of Test / Assignment with topic
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Shaikh Riyaj shaikh saheb. Dept. of Information Technology R.S.M. (Autonomous), Latur

Rajarshi Shahu Mahavidyalaya,Latur (Autonomous)