Shiv Chhatrapati Shikshan Sanstha's
Rajarshi Shahu Mahavidyalaya, Latur (Autonomous)
Department of I.T.
Teaching Plan

Timetable Duration : July 2024 to November 2024

Name of the Teacher : **Shri. R.S.S. Shaikh**

Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
B.Sc. C. A. I Semester	A	Digital Logic	101COA1501	30

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT I: Number System	12	05/07/2024 To 10/08/2024	Revision on Fundamental concepts	1
UNIT II: Basic and Universal Gate	8	16/08/2024 To 12/09/2024	Identification of gates	1
UNIT III: Adder, Mux and De-Mux	4	13/09/2024 To 20/09/2024	Seminar on Microprocessor	1
UNIT IV: Study of Flip flop	6	04/10/2024 To 21/10/2024	Assignment MCQ based	1

Teacher
Shaikh R.S.S.

Head
Dept. of Information Technology
R.S.M.(Autonomous), Latur

Principal
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Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
B.Sc. C. A. I Semester	B	Digital Logic	101COA1501	30

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT I: Number System	12	01/07/2024 To 08/08/2024	Revision on Fundamental concepts	1
UNIT II: Basic and Universal Gate	8	14/08/2024 To 10/09/2024	Identification of gates	1
UNIT III: Adder, Mux and De-Mux	4	11/09/2024 To 21/09/2024	Seminar on Microprocessor	1
UNIT IV: Study of Flip flop	6	02/10/2024 To 21/10/2024	Assignment MCQ based	1

Teacher

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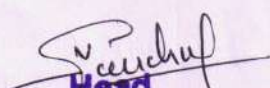
Name of the Teacher : **Shri. R.S.S. Shaikh**

Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
B.Sc. C.A.	C	Digital Logic	101C0A1501	30

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT I: Number System	12	03/07/2024 To 07/08/2024	Revision on Fundamental concepts	1
UNIT II: Basic and Universal Gate	8	14/08/2024 To 10/09/2024	Identification of gates	1
UNIT III: Adder, Mux and De-Mux	4	11/09/2024 To 19/09/2024	Seminar on Microprocessor	1
UNIT IV: Study of Flip flop	6	03/10/2024 To 24/10/2024	Assignment MCQ based	1

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Timetable Duration : December 2024 to March 2025

Name of the Teacher : **Riyaj Shaikh**

Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
B.Sc.CS T. Y. VI SEMESTER	A	Computer Science	Web Programming Using PHP	45

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT – I Introduction, Handling Forms with PhP	12	06/12/2024 To 11/01/2025	Practical Based Activity	1
UNIT II :- Function and Array	11	13/01/2025 To 14/02/2025	Class room Practice	1
UNIT III:- State management	10	17/02/2025 To 17/03/2025	Practice Bases Task	1
UNIT IV:- Introduction to OOPS and Database	12	18/03/2025 To 29/03/2025	Database Related Assignment	1

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Timetable Duration : December 2024 to March 2025

Name of the Teacher : **Riyaj Shaikh**

Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
B.Sc.CS T. Y. VI SEMESTER	B	Computer Science	Web Programming Using PHP	45

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT – I Introduction, Handling Forms with PhP	11	06/12/2024 To 08/01/2025	Practical Based Activity	1
UNIT – II Function and Array	12	13/01/2025 To 10/02/2025	Practical Based Activity	1
UNIT – III State Management	11	11/02/2025 To 12/03/2025	Practical Based Activity	1
UNIT - IV Introduction to OOPS and Database	11	13/03/2025 To 29/03/2025	Assignment and class test	1

Teacher
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Timetable Duration : December 2024 to March 2025

Name of the Teacher : **Riyaj Shaikh**

Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
BCA T.Y. VI Semester	A	Digital Image Processing Using MatLab	U-DIP-716	45

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT- I Introduction to DIP	11	07/12/2024 To 11/01/2025	Practical Based Activity	1
UNIT –II Digital Image Representation using Matlab	12	16/01/2025 To 13/02/2025	Class room Practice	1
UNIT- III Intensity transformation using MATLAB	11	14/02/2025 To 13/03/2025	Practice Based Task	1
UNIT –IV Frequency Domain Processing and Histogram Processing	11	14/03/2025 To 29/03/2025	Home Assignment	1

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Timetable Duration : December 2024 to March 2025

Name of the Teacher : **Riyaj Shaikh**

Details of Classes to be taught

Class	Division	Course Title	Course Code	Total Teaching Hrs
BCA T.Y. VI Semester	B	Digital Image Processing Using MatLab	U-DIP-716	45

Summary of Lesson Plan

Unit and Chapter to be Covered	Expected No. of Lectures	Date	Academic activities to be Organized	No. of Test / Assignment
UNIT- I Introduction to DIP	11	07/12/2024 To 11/01/2025	Practical Based Activity	1
UNIT II:- Introduction and Graphical Representation	12	13/01/2025 To 10/02/2025	Assignment and class test	1
UNIT- III Intensity transformation using MATLAB	12	11/02/2025 To 11/03/2025	Practical Based Activity	1
UNIT –IV Frequency Domain Processing and Histogram Processing	10	15/03/2025 To 29/03/2025	Home Assignment	1

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