

Rajarshi Shahu Mahavidyalaya (Autonomous), Latur
Teaching Plan (Semester-I)
(Sept 2021 to Dec 2021)

1. Details of Classes to be taught


Sr. No.	Class	Name of Assist. Prof.	Subject	Paper	Total Lecturers:
1	BSc. I Sem. I	Mr. Vijay Balaji Zadke	Electronics	AC Fundamentals and circuit Analysis Course Code: U-ELE-144	45 (2-credits)

2. Summary of Lesson Plan

Sr. No.	Unit and Chapter to be covered	Expected No. of Lectures	Date	Academic activities to be organized	No. of Test /Assignments with topic and date
1	Unit I:AC Fundamentals	12	21/09/21 to 07/10/21	ICT Based Teaching	-
2	Unit II:DC Network Theorems	12	8/10/21 to 20/11/21	ICT Based Teaching	Unit Test-I(data will be communicated later)
3	Unit III: series AC Circuits	15	22/11/21 to 20/12/21	ICT Based Teaching	-
4	Unit IV: Parallel AC Circuits	06	20/12/21 to 31/12/21	ICT Based Teaching	Unit Test-II (data will be communicated later)


Mr. V.B. Zadke

Date:20.09.2021


HoD
HEAD
Department of Physics & Electronics
Rajarshi Shahu Mahavidyalaya, Latur
(Autonomous)


Principal
PRINCIPAL
Rajarshi Shahu Mahavidyalaya
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Sr. No.	Class	Name of Assist. Professor	Subject	Paper	Total Lecturers:
1	B.Sc. I Sem. I	Mr. Vijay Balaji Zadke	Electronics	Semiconductor Devices - II U-ELE-145	45 (2-credits)

2. Summary of Lesson Plan

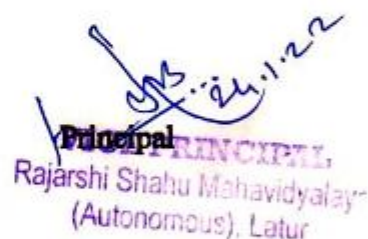
Sr. No.	Unit and Chapter to be covered	Expected No. of Lectures	Date	Academic activities to be organized	No. of Test / Assignment with topic and date
1	Unit I: Semiconductor Diode	12	24.09.21 to 10.10.21	ICT based teaching	--
2	Unit II: Special diodes	08	11.10.21 to 05.11.21	ICT based teaching	Activity based Unit Test-I (Date will be communicated later)
3	Unit III: Transistors	12	06.11.21 to 01.12.21	ICT based teaching	--
4	Unit IV: Field Effect Transistors	13	02.12.21 to 31.12.21	ICT based teaching	Unit Test II (Date will be communicated later)


HoD

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Date: 27-09-2021
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Teaching Plan (Semester-VI)

(December 2021 to April 2022)

1. Details of Classes to be taught

Sr. No.	Class	Name of Assist. Professor	Subject	Paper	Total Lecturers:
1	B.Sc. III Sem. VI	Mr. vijay balaji zadke	Eletronics	Digital Electronics-XI U-ELE-653	45 (2-credits)

2. Summary of Lesson Plan

Sr. No.	Unit and Chapter to be covered	Expected No. of Lectures	Date	Academic activities to be organized	No. of Test / Assignment with topic and date
1	Unit I: combinational circuits	13	20/12/21 to 22/01/22	--	--
2	Unit II: Flip-Flops	10	27/01/22 to 17/02/22	--	--
3	Unit III: Digital counters	15	18/02/22 to 26/03/22	--	Unit Test-I (1 to 7 Feb, 2022)
4	Unit IV: Shift Resisters	07	31/03/22 to 16/04/22	ICT teaching	Unit Test-II (03 to 09 March, 2022)


Mr. V.B.ZADKE

Date:20.12.2021


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