

Structure and Curriculum of B.Sc. (Degree) in Electronics Programme with Multiple Entry and Exit Options

Undergraduate Programme of Science and Technology B.Sc. (Degree) in Electronics

> Board of Studies in Electronics Rajarshi Shahu Mahavidyalaya, Latur (Autonomous)

w.e.f. June, 2023 (In Accordance with NEP-2020)

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Rajarshi Shahu Mahavidyalaya, Latur (Autonomous)

Academic Year: 2023-24

Review Statement

The NEP Cell reviewed the Curriculum of **B.Sc. (Degree) in Electronics** Programme to be effective from the **Academic Year 2023-24.** It was found that, the structure is as per the NEP-2020 guidelines of Govt. of Maharashtra.

Date: 09/08/2023

Place: Latur

NEP Cell Rajarshi Shahu Mahavidyalaya, Latur (Autonomous)

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CERTIFICATE

I hereby certify that the documents attached are the Bonafide copies of the Curriculum of **B.Sc. (Degree) in Electronics** Programme to be effective from the **Academic Year 2023-24**.

Date: 14/07/2023 Place: Latur

Dr. A. A. Yadav Chairperson Board of Studies in Electronics Rajarshi Shahu Mahavidyalaya, Latur (Autonomous)

शिव छत्रपती शिक्षण संस्था लातूर

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	Head, Department of Physics and		
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	Rajarshi Shahu Mahavidyalaya, La <mark>tur</mark>		
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4	Dr G.S. Shahane	Member	Expert from outside for
	DBF Dayanand college, Solapur		Special Course
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	Perfect Electronics, Dattanagar Wai,		
6	Satara.		
6	Dr Rangrao Suryawanshi	Member	P.G. Alumni
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/		Member	Faculty Member
	Rajarshi Shahu Ma <mark>havidya</mark> laya, Latur (Autonomous)	।राव छ	त्रपता
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9 10	Mr Swapnil Undalkar	Member	Member from Same Faculty
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11	Miss Vishakha Patil	Member	Member from Same Faculty
12	Mrs. Suraj Gund	Member	Member from Same Faculty
13		Meniber	Member Ironi Same Faculty

Latur (Autonomous)



"Electronics is clearly the winner of the day"

- John Ford

We have immense pleasure to share that our department is with the state-of-the-art facilities and has highly qualified and dignified faculty. This specific program is in accordance with NEP 2020 which enables electronics graduates to develop the technological and competitive skills needed in the design and operating modern telecommunication systems and networks. I take great pride in sharing that this programme follows outcome-based education in the teaching learning process. The department strives to provide a favorable environment for the students to develop electronic insights and practical skills and apply them to real world problems. In order to motivate the students, the department organizes regular trainings in various aspects of Electronics and to enrich their knowledge, the department arranges various workshops, national and international conferences every year. Faculty visits to leading universities in the globe are very much encouraged and appreciated. Awards, scholarships and recognitions speak a long way about the quality of faculty and students with the constant support and encouragements of the Management of the College.

Our Electronics curriculum which is in accordance with NEP 2020 integrates the Science and technology of all that makes communication through electronic devices. Electronics students design, build and manage systems that transmit process and store information as electrical or optical signals, addresses the critical challenges to face the society, industry and the academia. It is worthwhile to express our care and commitment to our students, guiding them to learn, grow, develop and achieve their goals in their pursuits so as to excel in their career in a every influencing domain. Let me take the opportunity to thank and wish you all a great success.





Index

Sr. No.	Content	Page No.
1	Structure for UG Programme	1
2	Abbreviations	2
3	Courses and Credits	3
4	UG Program Outcomes	4
5	Programme Specific Outcom <mark>es</mark>	5
6	Curriculum: Semester-I	7
7	Basket I: Generic/Open Elect <mark>ive (GE/OE</mark>)	17
8	Basket II: Skill Enhancement Courses (SEC)	18
9	Basket III: Ability Enhanceme <mark>nt Courses (AEC)</mark>	19
10	Curriculum: Semest <mark>er-</mark> II	21
11	Basket IV: Generic/Open Elective (GE/OE)	31
12	Basket V: Skill Enh <mark>ancement Courses (SEC)</mark>	32
13	Basket VI: Ability Enhancement Courses (AEC)	33
14	Extra Credit Activities	34
15	Examination Framework	36





(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Structure for Undergraduate Degree Programme in Electronics with Multiple Entry and Exit (In accordance with NEP-2020)

Year		Мај	or			VSC/	AEC/	OJT, FP, CEP,	Credi	Cum./Cr.
& Level	Sem	DSC	DSE	Minor	GE/OE	SEC (VSEC)	VEC	RP	t per Sem.	per exit
1 Level	2	3		4	5	6	7	8	<u> </u>	10
1		-	1						-	10
	I	DSC I:	NA	NA	GE-I:	VSC-I:	AEC-I	CC-I: 02 Cr.	22	
		04Cr.			04Cr.	02Cr.	MIL:	(NSS, NCC,		
		DSC II:				SEC-I:	02Cr.	Sports,		
		04Cr.				02Cr.	VEC-I:	Cultural)/		
							02Cr.	CEP-I: 02Cr.		
								(SES-I)/		
								0JT: 02Cr. /		44Cr.
								Mini		UG
T								Project:		Certifica
I 4.5								02Cr.		te
4.5	II	DSCIII:	NA	NA	GE-II:	VSC-II:	AEC-II	Generic IKS:	22	
		04Cr.			04Cr.	02Cr.	MIL:	02Cr.		
		DSC IV:				SEC-II:	02Cr.			
		04Cr.				02Cr.	VEC-II:			
							02Cr.			
	Cum.	16	_		08	04+04	02+02+	04	44	
	Cr.					= 08	02+02=	~		
				1		191	08	जपतो		

Exit Option: Award of UG Certificate in Major with 44 Credits and Additional 04 Credits Core NSQF Course/Internship or continue with Major and Minor

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Abbreviations:

- 1. DSC : Discipline Specific Core (Major)
- 2. DSE : Discipline Specific Elective (Major)
- 3. DSM : Discipline Specific Minor
- 4. GE/OE : Generic/Open Elective
- 5. VSEC : Vocational Skill and Skill Enhancement Course
- 6. VSC : Vocational Skill Courses
- 7. SEC : Skill Enhancement Course
- 8. AEC : Ability Enhancement Course
- 9. MIL : Modern Indian Lang<mark>uages</mark>
- 10. IKS : Indian Knowledge System
- 11. FSRCE : Fostering Social Responsibility & Community Engagement
- 12. VEC : Value Education Courses
- 13. OJT : On Job Training
- 14. FP : Field Projects
- 15. CEP : Community Engagement and Service
- 16. CC : Co-<mark>Curricula</mark>r Cour<mark>ses</mark>
- 17. RP : Research Project/Dissertation
- 18. SES : Shahu Extension Services

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Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur

(Autonomous) Faculty of Science and Technology Department of Physics and Electronics B.Sc. (Degree) in Electronics

Year & Level	Semester	Course Code	Course Title	Credits	No. of Hrs.
		101ELE1101	AC Fu <mark>ndam</mark> entals and Circuit	03	45
		(DSC-I)	Anal <mark>ysis-I</mark>		
		101ELE1103	Lab <mark>Cours</mark> e-I	01	30
		101ELE1102	Sem <mark>iconduc</mark> tor Devices and	03	45
		(DSC-II)	Inst <mark>rumentat</mark> ion-II		
		101ELE1104	Lab <mark>Course-II</mark>	01	30
	Ι	101ELE1501	Dom <mark>estic Electrical A</mark> ppliances	02	45
		(VSC-I)	and t <mark>heir Maintenance</mark>		
		GE-I	From <mark>Basket</mark>	04	60
		(SEC-I)	Fro <mark>m Basket</mark>	02	30
		(AEC-I)	Fro <mark>m Basket</mark>	02	30
		(VEC-I)	Constitution of India	02	30
		AIPC/O <mark>JT-I</mark>	Field Project	02	60
I		Tot	tal Credits	22	
4.5		101ELE2101	Power Supplies and Active	03	45
1.5		(DSC-III)	Filters-III		
		101ELE2103	Lab Course-III	01	30
		101ELE2102	Amplifiers and Number	03	45
		(D <mark>SC-IV)</mark>	Systems-IV		
		101ELE2104	La <mark>b Cou</mark> rse-IV	01	30
	II	101ELE2501	Mobile Repairing	02	45
	11	(VSC-II)	लातर		
		GE-II	From Basket	04	60
		(SEC-II)	From Basket	02	30
		(AEC-II)	From Basket	02	30
		(VEC-II)	FSRCE (CBPR)	02	30
	R	Generic IKS	Introduction to Indian	02	60
		Later	Knowledge System		
			tal Credits	22	
	Т	otal Credits (S	emester I & II)	4	4



(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

	Programme Outcomes (POs) for B.Sc. Programme
P01	Disciplinary Knowledge: Comprehensive knowledge of science subjects which constitute the graduate programme and execution of scientific knowledge in the specific area.
PO2	Scientific Outlook: The qualities of a science graduate such as observation, precision, analytical mind, logical thinking, clarity of thought and expression and systematic approach.
P03	Self-Directed Life-long Learning: Ability to appear for various competitive examinations or choose the post graduate programme or other related programme of their choice.
PO4	Research Skills: Functional knowledge and applications of instrumentation and laboratory techniques to do independent experiments, interpret the results and develop research ethos.
PO5	Problem Solving Skills: Analytical and logical skills and critical thinking to extract information from qualitative and quantitative data, formulate and solve problems in a systematic and rational manner.
P06	Professional Competence and Ethics: Aptitude and skills to perform the jobs in diverse fields such as science, engineering, industries, survey, education, banking, development and planning, business, public service, self-business etc. with human rationale and moral values.

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(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Р	rogramme Specific Outcomes (PSOs) for B.Sc. (Degree) Electronics
PSO No.	Upon completion of this programme the students will be able to
PSO1	Academic Competence: Get knowledge in AC Fundamentals and Circuit
	Analysis, Semiconductor De <mark>vices</mark> , Power Supplies and Active Filters,
	Amplifiers and Number Sy <mark>stem</mark> s, Oscillators, Multivibrators and Sweep
	Circuits, Power Electronic Devices, Fundamentals of Digital Electronics,
	Power Electronic Devi <mark>ce A</mark> pplications, Communication Electronics,
	Operational Amplifier and its Applications, Digital Electronics.
PSO2	Scientific Outlook: Perfo <mark>rm exper</mark> iments in Electronics, Demonstrate the
	working of transistor in <mark>three diffe</mark> rent regions, uses of semiconductor
	devices, Illustrating the id <mark>ea of resonance</mark> in electric circuits, working of
	inverter. Exhibit independe <mark>nt thinking and s</mark> cientific temper based on facts,
	observation and experiment <mark>al work.</mark>
PSO3	Personal and P <mark>rofessional Competence:</mark> Assemble circuits and build
	designs for indu <mark>str</mark> ial applications. Apply Welding control and its
	applications. Oper <mark>ate b</mark> asic and advanced Instruments, equipment and tools.
	Analyse the functi <mark>oning of sensors, actuators, line</mark> ar and digital ICs.
PSO4	Entrepreneurial Competence: Design and develop Electronics systems in
	computers, ag <mark>riculture, medical, consumer products, a</mark> nd transportation.
	Work to com <mark>bine hardware and Software to solve real life p</mark> roblems with IoT.
PSO5	Research Competence: Incorporate and apply the knowledge in Electronics
	to solve problems in various branches of knowledge and day-to-day human
	affairs. Create, follow, use and apply advanced tools and techniques for
	research in Electronics. Ut <mark>ilize Matlab</mark> and Scilab tools for research.
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Semester-I

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(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: DSC-I Course Title: AC Fundamentals and Circuit Analysis-I Course Code: 101ELE1101 Credits: 03 Max. Marks: 75

Lectures: 45 Hrs.

Learning Objectives:

LO1. To develop understanding about generation of single-phase AC, definitions pertaining to alternating quantities,

LO2. To clear the concepts of average and RMS values, determination of RMS and average value for different types of waveforms,

LO3. To inculcate the knowledge about Kirchhoff's laws, voltage and power using Mesh and nodal analysis,

LO4. To develop the strong foundation for electrical networks,

LO5. To develop analytical qualities in electrical circuits by application of various theorems,

LO6. To illustrate the idea of r<mark>eso</mark>nance in series LCR and parallel LCR electric circuits LO7. To develop the skill for solving electrical networks problems.

Course Outcomes:

After completion of course, students will be able to-

CO1. Have strong basics for network theory,

CO2. Analyze and solve electric circuits

CO3. solve complicated networks by application of theorems,

CO4. Understand and use the concept of impedance and reactance to analyze simple ac series circuits,

CO5. Calculate the impedance, phase angle, power, power factor, voltage and/or current in series RLC circuit

CO6. Draw the relevant phasor diagrams and waveform diagrams of voltage and current, for pure resistance, inductance and capacitance.

Unit No.	Title of Unit & Contents	Hrs.
Ι	A.C. Fundamentals	12
	 Introduction, Generation of Alternating Voltage and Currents, Equation of Alternating Voltage and Current, Alternate Method for The Equations of Alternating Voltages and Currents, Simple and Complex Waveforms, Cycle, Time Period, Frequency and Amplitude, Different Forms of Emf Equations, Phase and Phase Difference, Definition of RMS Value, Average Value, Form Factor, Peak Value and Amplitude Factor, AC Through Resistance, Inductance and Capacitance. 	

	Unit Outcomes: U01. Have strong basics for network theory,				
	U02. Construct experimental AC circuits using schematics and				
	perform tests and measurements with a multimeter and signal				
	generator.				
II	DC Network Theorems	12			
	1. Introduction, Electric Circuits,				
	2. Kirchhoff's Laws, Determination of Voltage Polarities,				
	Assumed Direction of Current,				
	3. Ideal Constant Voltage Source, Ideal Constant Current Source,				
	Practical Constant Voltage Source, Practical Constant Current				
	Source,				
	4. Thevnin's Theorem, Norton's Theorem, Superposition Theorem, Maximum Power Transfer Theorem, Duality				
	Theorem,				
	5. Delta/Star and Star/Delta Transformation.				
	6. Numerical Problems.				
	Unit Outcome:				
	UO1. be able to solve comp <mark>licated networks</mark> by application of				
	theorems.				
III	Series AC Circuits	15			
	1. Introduction, AC Through Resistance, Inductance and				
	Capacitance, AC Through R & L,				
	2. Power Factor, Active and Reactive Components of Circuit				
	Current (I),				
	3. Active, Reactive and Apparent Power, Q Factor of Coil, 4. AC Through RC, AC Through R-L And C, Resonance In R-L-C				
	Circuit,				
	5. Graphical Representation of Series Resonance, Resonance				
	Curve, Half Power Band-Width of A Series LCR Resonant				
	Circuit,				
	6. Q Factor <mark>of a Serie</mark> s LCR <mark>Reson</mark> ant Circuit,				
	7. Numerical Problems.				
	Unit Outcomes: [위석이 관관리]				
	UO1. Calculate the impedance, phase angle, power, power				
	factor, voltage and/or current in series RLC circuit,				
	UO2. Understand and use the concept of impedance and reactance to analyze simple ac series circuits,				
IV	Parallel AC Circuits	06			
-	1. Introduction				
	2. Resonance in Parallel LCR Circuits,				
	3. Graphical Representation of Parallel LCR Resonant Circuit,				
	Band Width of a Parallel LCR Resonant Circuit,				
	4. Q Factor of a Parallel LCR Resonant Circuit, Series-Parallel				
	Circuits.				
	5. Numerical Problems.				
	Unit Outcomes:				
	UO 1. Draw the relevant phaser diagrams and waveform				
	diagrams of voltage and current, for pure resistance,				

inductance and capacit UO 2. Demonstrate the phase RLC in frequency doma	and amplitude information of
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Learning Resources:

1. A text book of electrical technology Vol-I: B.L. Theraja, A.K. Theraja, S. Chand and Company Ltd. Ramnagar, New Delhi. (Reprint 2010)

2. Basic Electronics: Solid State - B.L. Theraja, S. Chand and Company Ltd. Ramnagar, New Delhi (2009

3. A text book of electrical technology, by B.L. Theraja Vol. I, Nirja Construction and Development Company

4. Basic Electronics, Bernard Grob, Ta<mark>ta Mc-</mark>Graw Hill Publications (2007) Tenth Edition 5. A Text book applied electronics – R.<mark>S. Sedha</mark>, S. Chand and Company Ltd. (2004).

6. Electronic Devices and Circuits: An Introduction- Allen Mottershead-PHI Pvt. Ltd, New Delhi.

7. Electronic Fundamentals and Applications (Integrated and Discrete system), John D. Ryder (1989) Prentice Hall of India, Pvt. Ltd. New Delhi – 110001.

8. Electrical machines and Appliances theory-Tamilnadu Textbook corporation,College Road, Chennai - 600 006

9. Electric Principles, Third Edition, A.P. Malvino, Tata McGraw-Hill Publications

10. Principal of Electronics, V.K.Mehta and Rohit Mehta, S. Chand and Company Ltd. Ramnagar, New Delhi. (Reprint 2010).





(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: Lab Course

Course Title: Lab Course –I (Based on DSC-I)

Course Code: 101ELE1103

Credits: 01

Max. Marks: 50

Hours: 30

Leaning Objectives

- LO1. To understand the use of basic instruments such as: Voltmeter, Ammeter, Multimeters, Signal generator, CRO, etc.
- LO2. To Study the characteristics and use of various semiconductor devices and electronic component such as diode, LED, JFET, Zener diode, Photodiode R, L, C and use of color code formula.
- LO3. To verify some network theorems such as maximum power transfer theorem and Thevenin's theorem.
- LO4. Study of series resonance
- LO5. Determination of values of resistors using color code formula and verification of it by multimeter.

Course outcomes

After completion of course, students will be able to-

- CO1. Uses of semiconductor devices such as diode, LED, JFET, Zener diode, Photodiode,
- CO2. Handling and making use of CRO and signal generator for the measurement of frequency, time, amplitude, phase of signal also to differentiate AC and DC with the help of CRO,
- CO3. Handling the multimeters for various purposes such as for measurement of AC, DC, Resistance, Testing of continuity of circuit and semiconductor devices,
- CO4. Applying the circuit theorems for the determination of circuit current through resistance and voltage across the same
- CO5. Illustrating the idea of resonance in electric circuits.

Practical	Unit
No.	
1	Verification of maximum power transfer theorem for DC Circuits.
2	Verification of Thevenin's theorem for DC Circuits
3	D <mark>etermination of values of given resistors by using colo</mark> ur code and
	verification of them by multimeter.
4	Determination of amplitude, frequency and time period of given
	waveform using CRO.
5	Study of LED Characteristics
6	Study of Zener diode reverse characteristics
7	Study of Photodiode reverse characteristics
8	Study of LCR Series resonance circuit (digital AC current meter)
9	Study of LCR parallel resonance circuit (digital AC current meter)

N.B.: Any Six Practical from above.



(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: DSC-II Course Title: Semiconductor Devices-II Course Code: 101ELE1102 Credits: 03 Max. Marks: 75

Lectures: 45 Hrs.

Learning Objectives

- LO1. To inculcate the knowledge about the components used in electronics, such as resistances, capacitors, diodes, transistors, UJT, FET, MOSFET and others.
- LO2. To develop the measurement ability among the students about the various electronic components.
- LO3. To make students familiar about the measurements of voltage, current, resistance, AC as well as DC using multi meters.
- LO4. To make students familiar about the measurements of voltage and frequencies of the waves using CRO.
- LO5. To develop the measurement ability among the students about the various electronic Instruments like Dual power supply, Function Generators.
- LO6. To develop the strong foundation for electrical networks,

Course outcomes

After completion of the course, students will be able to-

- CO1. Select and measure the electronic components like resistors, capacitors, diodes, transistors, UJT, FET, MOSFET, etc.
- CO2. Handle multimeters, CRO and VTVMs and able to make accurate measurements.
- CO3. Know the characteristics of diodes and transistors.
- CO4. Identify different models of BJT, regions of operations, and their IVcharacteristics.
- CO5. Demonstrate the knowledge of MOSFET region models and their IVcharacteristics.
- CO6. Interpret various applications of diode.

CO7. Design a simple BJT bias circuit for a given specification.

Unit No.	Title of Unit & Contents	Hrs.
Ι	Semiconductor Diode	12
	1. The Unbiased Diode, Forward Bias, Reverse Bias, 10 Jon	
	2. V–I Characteristics of Diode,	
	3. Energy Levels, The Energy Hills, Barrier Potential and	
	Temperature,	
	4. Basic Ideas; Basic Diode Circuit, Forward Region, Knee	
	Voltage, Maximum DC Forward Current,	
	5. The Ideal Diode, The Second Approximation, The Third	

	Approximation,	
	6. Bulk Resistance, DC or Static Resistance of Diode, Dynamic	
	or AC Resistance of Diode. Load Lines.	
	7. Numerical Problems.	
	Unit Outcomes:	
	U01. Analyze the characteristics and theories in semiconductor	
	materials in terms of crystal structures, charge carriers and	
	energy bands.	
	UO2. Explain how to find the fermi energy level and carrier	
	density in n-type and p-type semiconductors.	
II	Special Diodes	08
	1. Introduction,	
	2. Zener Diode,	
	3. Tunnel Diode, Varactor Diode,	
	4. PIN Diode, Schottky Diode	
	5. Light Emitting Diode, Photodiode,	
	6. Uses of Each Diode (Qualitative Analysis),	
	7. Optoelectronic Devices.	
	Numerical Problems,	
	Unit Outcomes:	
	U01. Know the ch <mark>aracteristics of various diodes.</mark> U02. Interpret various applications of diode.	
III	Transistors	12
	1. Introduction, Bipolar Junction Transistor, The Unbiased	
	Transistor,	
	2. Transistor Biasing, Important Biasing Rules, FF, RR, FR	
	Biasing, Voltage Divider Bias,	
	3. Transistor Circuit Configurations, CB And CE Configurations,	
	Relation Between A and B, Relation Between Transistor	
	Currents,	
	4. Transistor Characteristics In C-E, C-B And C-C Configurations,	
	5. Numerical Problems.	
	Unit Outcomes:	
	U01. Design a simple BJT bias circuit for a given specification	
	UO2. Identify different models of BJT, regions of operations, and	
	their IV-characteristics	
IV	Field Effect Transistors	13
	1. Introduction,	
	2. J-FET: Construction, Operation,	
	3. Static Characteristics of JFET, JFET Drain Characteristics with	
	V _{GS} =0, JFET Characteristics with External Bias, Transfer	
	Characteristics,	
	Characteristics, 4. Small Signal JFET Parameters, Common Source JFET As an	

Power FET's,	
5. Depletion Enhancement -MOSFET, Schematic Symbols for A	
Depletion Enhancement -MOSFET,	
6. Static Characteristics of Depletion Enhancement-MOSFET,	
Enhancement Only N-Channel MOSFET And Its Transfer	
Characteristics,	
Unit Outcomes:	
UO1. Analyze (calculate voltages and currents) a simple	
MOSFET (JFET) bias circuit and find its Q-point.	
UO2. Demonstrate the knowledge of MOSFET (JFET) region	
models and their IV-characte <mark>risti</mark> cs.	

Learning Resources:

- 1. Electronic Principles, Sixth Edition, A.P. Malvino, Tata McGraw-Hill Publications (Multicolor Illustrative Edition)
- 2. Basic Electronics Solid State: B.L. Theraja, S. Chand and Company Ltd.
- 3. A Text Book of Applied Electronics: R. S. Sedha (2004), S. Chand and Company Ltd. Ramnagar, New Delhi.
- 4. Electronic Fundamentals and Applications (Integrated and Discrete system), John D. Ryder (1989) Prentice Hall of India, Pvt. Ltd. New Delhi – 110001
- 5. Electric Principles, Third Edition, A.P. Malvino, Tata McGraw-Hill Publications
- 6. Electronic Devices and Circuits: An Introduction- Allen Mottershead-PHI P. Ltd, New Delhi
- 7. Principal of Electronics, V.K. Mehta and Rohit Mehta, S. Chand and Company Ltd.
- 8. Electronic Devices and Circuits: An Introduction- Allen Mottershead-PHI P. Ltd, New Delhi.
- 9. Basic Electronics, Bernard Grob, Tata Mc-Graw Hill Publications (2007) Tenth Edition
- 10. Electric Principles, Third Edition, A.P. Malvino, Tata McGraw-Hill Publications

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(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: Lab Course Course Title: Lab Course –II (Based on DSC-II) Course Code: 101ELE1104 Credits: 01 Max. Marks: 50

arks: 50

Hours: 30

Learning Objectives

- LO1. To understand the use of basic Laws such as: Ohm's law, Kirchhoff's Law, etc.
- LO2. To Study the characteristics and use of various semiconductor devices and electronic component such as CB NPN Transistor, P-N diode, Tunnel diode.
- LO3. To verify some network theorems such as Superposition Theorem.
- LO4. Study of series MOSFET.
- LO5. Study of Capacitor Characteristics and Inductor Characteristics.

Course outcomes

After completion of the cours<mark>e, students will be able to-</mark>

- CO1. Uses of semiconductor devices such as CB NPN Transistor, P-N diode, Tunnel diode
- CO2. Handling and making use of CRO and signal generator for the measurement of frequency, time, amplitude, phase of signal also to differentiate AC and DC with the help of CRO
- CO3. Handling the multimeter for various purposes such as for measurement of AC, DC, Resistance, Testing of continuity of circuit and semiconductor devices
- CO4. Applying the circuit theorems for the determination of circuit current through resistance and voltage across the same
- CO5. Illustrating the idea of Capacitor, Inductor Characteristics.

Practical No.	Unit
1	Verification of Norton's theorem
2	Verification of Kirchhoff's Law.
3	Verification of Superposition Theorem.
4	Characteristics of Tunnel diode.
5	I/O and transfer characteristics of CE mode of NPN Transistor.
6	V-I Characteristics of P-N diode.
7	Study the V-I Characteristics of JFET.
8	Study the RC Circuit for Charging and Discharging of Capacitor through
	Resistor.
9	Study of Growth and decay of current in L-R Circuit.

N.B.: Any Six Practical's from above.



(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: VSC-I Course Title: Domestic Electrical Appliances and their Maintenance Course Code: 101ELE1501 Credits: 02 Max. Marks: 50 Lectures: 30 Hrs.

Learning Objectives:

- LO1. To understand the construction, working and types of Domestic Electrical Appliances.
- LO2. To understand maintenance details of Domestic Electrical Appliances
- LO3. Check the electrical connections at house-hold but will also learn the skill to repair the electrical appliances for the general troubleshoots and wiring faults.
- LO4. Understand the working principles of different household domestic appliances.

Course Outcomes:

After completion of the cours<mark>e, students will be able to-</mark>

- CO1. Develop detailed know<mark>led</mark>ge about few Domestic Electric Appliances
- CO2. Enhance maintenance skill of Domestic Electric Appliances
- CO3. To test and detect faults in home appliance and Establish Self business
- CO4. Identify various parts of home appliances product
- CO5. To realize basic working principal of home appliances.

Unit No.	Title of Unit & Contents	Hrs.
Ι	Basic Electronics	07
	 Electrical Conductors and Insulators, Voltage, Current, Resistance, Ohm's Law, Capacitance, Inductance, Series and Parallel Combinations of Resistors, Galvanometer, Ammeter, Voltmeter, Multimeter, Transformers, Electrical Energy, Power, Watt, Kilowatt Hour (kWh), Horse Power, Consumption of Electrical Power Unit Outcomes: U01. To understand the working principles of different household domestic appliances U02. Acquire working knowledge on multimeters, galvanometers, ammeters, voltmeters, ac/dc generators, transformers. 	
II	Domestic Electric Appliances	08
	 Construction, Working and Types: Electric Iron, Water Heaters, Electric Kittle, Coffee Maker, Working and Types: Electric Mixer, Egg Beaters, Electric Fan, Hair 	

	Drier.	
	Unit Outcomes:	
	UO1. Develop detailed knowledge about few Domestic Electric	
	Appliances	
	UO2. To realize basic working principal of home appliances	
III	Safety Precautions and Maintenance	06
	1. Introduction,	
	2. Tools for Maintenance: Electric Tester, Screw Driver, Nut Driver,	
	Benches, Wrenches,	
	3. Tools for Maintenance: Hammers, Pliers, Cutters, Safety	
	Precautions While Handling Tools and Repairing Appliances, Etc.	
	4. Importance of Earthing.	
	Unit Outcome:	
	UO1. To test and detect faults in home appliance and Establish Self	
	business	
IV	Practical (Included in above 04 units)	08
	1. Testing and repair of El <mark>ectric Kittle.</mark>	
	2. Testing and r <mark>ep</mark> air of E <mark>lectric Fan.</mark>	
	3. Testing and r <mark>epa</mark> ir of E <mark>gg Beater.</mark>	
	4. Testing and r <mark>epair</mark> of Coffee Maker.	
	5. Testing and r <mark>epair of Electric Mixer.</mark>	
	6. Testing and repairs of Hair Drier.	
	7. Testing and repairs of Water Heater.	
	8. Dismantling and reassembling of Ordinary type and	
	Automatic/Thermostat control type Electric Iron.	

Learning Resources:

- 1. Troubleshooting and Repair of Appliances-Eric Kleinert, 3rd Edition, 2012, Mc-Graw Hill Publishers
- 2. Study of Electrical Appliances & Devices-K.B. Bhatia (ISBN: 978-93-87394-22-3)
- 3. Fundamentals of Maintenance of Electrical Equipments- K.B. Bhatia (ISBN: 978-93-87394- 31-5)
- 4. Electrical Machines and Appliances theory-Tamilnadu Textbook corporation, College Road, Chennai - 600 006.
- 5. A Text book on Electrical Technology, B.L. Theraja, S. Chand& Co.
- 6. A Text book on Electrical Technology, A.K. Theraja., S. Chand& Co.
- 7. Performance and design of AC machines, M.G. Say, ELBS Edn.
- 8. Consumer Electronics, S.P. Bali, Pearson.
- 9. Domestic Appliances Servicing, K.P. Anwer, Scholar Institute Publications.
- 10. Handbook of Repair & Maintenance of domestic electronics appliances; BPB Publications.



Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur (Autonomous) UG First Year

Basket I: Generic/Open Elective (GE/OE)

(GEs offered to the Science & Technology students in Sem.-I)

Sr.	BoS Proposing	Code	Course Title	Credits	Hrs.
No.	GE/OE				
1	Commerce	101AAF <mark>1401</mark>	Mutual Fund	04	60
			Management		
2	Commerce	101MA <mark>E1401</mark>	Fundamentals of	04	60
			Statistics		
3	English	101EN <mark>G1402</mark>	English for Science and	04	60
			Technology		
4	Geography	101GE <mark>01401</mark>	General Geography	04	60
5	Commerce	101BAI <mark>1401</mark>	Personal Financial	04	60
			Management		
6	Marathi	101MAR1401	स्पर्धा परीक्षा आणि मराठी	04	60
			भाषा		
7	Political Science	101POL1401	Human Rights	04	60
8	Biotechnology	101BI01401	Nutrition, Health and	04	60
			Hygiene		
9	Music	101MUS1401	Indian Vocal Classical &	04	60
			Light Music		
10	NCC Studies	101NCC1401	Introduction to NCC	04	60
11	Sports	101SP01401	Counseling and	04	60
			Psychotherapy		

Note: Student can choose any one GE from the basket.

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Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur (Autonomous) UG First Year

Basket II: Skill Enhancement Courses (SEC)

(SEC offered to the Science & Technology students in Sem.-I)

Sr. No.	BoS Proposing SEC	Code	Course Title	Credits	Hrs.
1	Chemistry	101CHE1601	Pesticides and Green Chemistry	02	30-45
2	Information Technology	101COM16 <mark>01</mark>	Basics of Python Programming	02	30-45
3	Physics	101PHY16 <mark>01</mark>	Physics Workshop Skills	02	30-45
4	Biotechnology	101BIO160 <mark>1</mark>	Food Processing Technology	02	30-45
5	Botany	101B0T160 <mark>1</mark>	Mushroom Cultivation Technology	02	30-45
6	English	10 <mark>1EN</mark> G16 <mark>01</mark>	Proof Reading and Editing	02	30
7	Information Technology	1 <mark>01COA1601</mark>	PC Assemble and Installation	02	30-45
8	Marathi	101MAR1601	कथा/पटकथालेखन	02	30
9	Zoology	101Z001601	Bee Keeping	02	30-45

Note: Student can choose any one SEC from the basket.

Raiarshi Shahu Mahavidvala

Latur (Autonomous)

शव छत्रपती

क्षण संस्था



Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur (Autonomous) UG First Year Sem I

Basket III: Ability Enhancement Courses (AEC)

(AEC offered to the Science & Technology students in Sem.-I)

Sr. No.	BoS Proposing AEC	Code	Course Title	Credits	Hrs.
1	Marathi	101MAR1701	भाषिक कौशल्य भाग – १	02	30
2	Hindi	101HIN17 <mark>01</mark>	हिंदी भाषा शिक्षण भाग – १	02	30
3	Sanskrit	101SAN1701	व्यावहारीक व्याकरण व नितिसुभाषिते	02	30
4	Pali	101PAL17 <mark>01</mark>	उपयोजित व्याकरण	02	30



Semester-II

शिव छत्रपती शिक्षण संस्था लातूर

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(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: DSC-III Course Title: Power Supplies and Active Filters III Course Code: 101ELE1201 Credits: 03 Max. Marks: 75

Lectures: 45 Hrs.

Learning Objectives:

LO1. To develop understanding about the power supplies using AC mains,

LO2. To inculcate the idea about the transformer and its working,

LO3.To develop the skill of design of Regulated power supplies of different ratings and voltage ranges,

LO4. To make students familiar about three terminal regulators and IC regulators of variable power supply voltages,

LO5. To inculcate the idea about various filters like R-L filter, R-C pass band filter, Band reject filters, band stop filters, low pass filters and high pass filters.

Course Outcomes:

After completion of the cours<mark>e, students will be able to-</mark>

CO1. Construct the necessary power supplies of different ratings,

CO2. Construct and use transformers,

CO3. Explain usefulness of wave filter and their uses in electronic circuitry.

CO4. Design regulated power supplies

Unit No.	Title of Unit & Contents	Hrs.
Ι	Transformers	12
	 Introduction, Working Principle of a Transformer, Transformer Construction, Elementary Theory of An Ideal Transformer, Emf Equation of a Transformer, Voltage Transformation Ratio Emf Equation of a Transformer, Voltage Transformation Ratio. Condition for Maximum Efficiency, Auto Transformer Numerical Problems. 	
II	Unit Outcomes: UO1. Learn how to analyses circuits involving linear and ideal transformers., UO2. Be familiar with ideal transformer Unregulated Power Supplies	12
	1. Introduction,	

	U01. Explain usefulness of wave filter and their uses in electronic circuitry. U02. Emphasize the importance of filter circuits.	
	5. Numerical Problems. Unit Outcomes:	
	Filter.	
	R- L Filter, High Pass R-C Filter, 4. High Pass R-L Filter, R-C Band Pass Filter, R-C Band Stop	
	3. Different Types of Wave Filters, Low Pass RC Filter, Low Pass R- L Filter, High Pass R-C Filter	
	2. Applications,	
	1. Introduction	
IV	Wave filters	09
	UO2. Student shall be able to construct and test Zener diode as voltage regulator.	
	Zener diode.,	
	U01. Able to observe and draw the static characteristics of a	
	6. Numerical Problems.	
	Using IC 74XX And 79XX, Adjustable Voltage Regulator Using IC LM 317,	
	5. Monolithic or IC Voltage Regulator, Fix Voltage Regulator	
	3. Control Transistor Series Regulator, 4. Transistor Shunt Voltage Regulator,	
	Regulator,	
	2. Zener Diode Shunt Re <mark>gulator, T</mark> ransistor Series Voltage	
	1. Introduction, Voltage Regulation,	
III	Regulated Power Supplies	12
	understand the operation of rectifier circuit.	
	Unit Outcomes: UO1. Construct the necessary power supplies of different	
	6 Numerical Problems.	
	5. Filters: Series Inductor Filter, Shunt Capacitor Filter, LC Filter, C-L-C (π) Filter	
	4. Half Wave Rectifier, Full Wave Rectifier, Full Wave Bridge Rectifier,	
	3. Steady & Pulsating DC Voltages, and Rectifiers,	

Learning Resources:

1. A Text Book of Electrical Technology (SI Units), Vol II- B.L. Theraja, Publication Division (U-I) of Nirja Construction & Development Company Pvt. Ltd.

- 2. Basic Electronics Solid State, B.L. Theraja (2009) S. Chand and Company Ltd. Ramnagar, New Delhi
- 3. A Text Book of Electrical Technology (in SI Units) Vol. I, B.L. Theraja, A.K. Theraja, (2010) S. Chand and Company Ltd. Ramnagar, New Delhi
- 4. A text book of electrical technology Vol-I: B.L. Theraja, A.K. Theraja, S. Chand and Company Ltd. Ramnagar, New Delhi. (Reprint 2010)
- 5. Basic Electronics: Solid State B.L. Theraja, S. Chand and Company Ltd. Ramnagar, New Delhi (2009
- 6. A text book of electrical technology, by B.L. Theraja Vol. I, Nirja Construction and Development Company
- 7. Basic Electronics, Bernard Grob, Tata Mc-Graw Hill Publications (2007) Tenth Edition
- 8. A Text book applied electronics R.S. Sedha, S. Chand and Company Ltd. (2004).
- 9. Electronic Devices and Circuits<mark>: An Intro</mark>duction- Allen Mottershead-PHI P. Ltd, New Delhi
- 10. Principal of Electronics, V.K.Me<mark>hta and Rohit M</mark>ehta, S. Chand and Company Ltd. Ramnagar, New Delhi. (Reprint 2010).





(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: Lab Course Course Title: Lab Course – III (Based on DSC-III) Course Code: 101ELE1203 Credits: 01 Max. Marks: 50

Hours: 30

Leaning Objectives

LO1. To inculcate the idea of rectification with the help of P-N junction diode and types of rectification,

LO2. To develop the understanding about voltage regulation using Zener shunt regulator and transistor series regulator,

LO3. To familiarize students with frequency filters such as RC Low pass and High pass filter,

LO4. To develop better idea of transformer (Static devices),

LO5. To equip the students with transistor characteristics and transistor amplifier under CE configuration.

Course outcomes

After completion of the course, students will be able to-

CO1. Full process of rectification, Difference between AC and DC,

CO2. Is the rectification being sufficient for the conversion of AC and DC?

CO3. The working of transistor in three different regions (Cut off, Saturation and active),

CO4. How transistor is used for amplification of weak signals.

Practical	Unit
No.	
1	Study of step-up transformer
2	Study of step-down transformer
3	Study of half wave r <mark>ectif</mark> ier with and without filter.
4	Study of full wave rectifier with and without filter
5	Study of Zener shunt regulator
6	Study of Transistor series regulator
7	Study of passive low pass RC filter
8	Study of passive high pass RC filter
9	Study of Voltage regulation using IC74XX and IC79XX

N.B.: Any Six Experiments from above. Utonomous)



(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: DSC-IV Course Title: Amplifiers and Number Systems-IV Course Code: 101ELE1202 Credits: 03 Max. Marks: 75

Lectures: 45 Hrs.

Learning Objectives:

LO1. To develop the concepts about operating point,

LO2. To develop the knowledge about types of biasing and its usefulness,

LO3. To introduce small signal behavior of transistors,

LO4. To inculcate the knowledge abo<mark>ut h parameter</mark> equivalent circuits for the three transistor configurations CE, CB, CC,

LO5. To familiarize the concepts of feedback amplifiers,

LO6. To familiarize with different number systems and their applications.

Course Outcomes:

After completion of the cours<mark>e, students will be able to-</mark>

CO1. Draw AC-DC load line and evaluate different parameters of amplifier,

CO2. Explore use of biasing circuits in various applications,

CO3. Solve the problems on small signal amplifiers,

CO4. Draw the h parameter equivalent circuits for the transistor configurations CE, CB, CC,

CO5. Compare CC, CE and CB with respect to Ri, Ro, Ai, and Av.

CO6. Compare the four negative feedback topologies,

CO7. Represent numerical values in various number systems and perform binary arithmetic and conversions between different number systems,

CO8. Measure the bandwidth of an amplifier from a graph of voltage gain against frequency.

Unit No.	Title of Unit & Contents	Hrs.
Ι	Transistor Biasing	12
	 Introduction, DC Load Line, Q Point and Maximum Undistorted Output, AC Load Line, Need for Biasing a Transistor, Factors Affecting Bias Variations, Stability Factor, Beta Sensitivity, Stability Factor for CB & CE Circuits, Different Methods for Transistor Biasing: Base Bias with Emitter Feedback, 	

	8. Voltage Divider Bias,	
	9.Numerical Problems.	
	Unit Outcomes: UO1. Draw AC-DC load line and evaluate different parameters of amplifier, UO2. Explore use of biasing circuits in various applications,	
II	Small Signal Amplifiers	12
	1. Introduction,	
	2. Hybrid Parameters,	
	3. AC Equivalent Circuit Using <mark>h-P</mark> arameters,	
	4. Transconductance Model <mark>, Anal</mark> ysis of CE Amplifier,	
	5. CB Amplifier,	
	6. CC Amplifier Using h Par <mark>ameters,</mark>	
	7. Numerical Problems.	
	Unit Outcomes:	
	UO1. Solve the problems on small signal amplifiers,	
	UO2. Draw the h parameter equivalent circuits for the transistor configurations CE, CB, CC.	
III	Feedback Amplifiers	12
	 2. Principle of Feedback Amplifiers, 3. Advantages of Negative Feedback: Gain Stability, 4. Decreased Distortion, 5. Increased Bandwidth, 6. Forms of Negative Feedback: Current – Series Feedback Amplifier, 7. Voltage Series Negative Feedback Amplifier, 8. Numerical Problems Unit Outcomes: 	
	UO1. Compare CC, CE and CB with respect to Ri, Ro, Ai, and Av. UO2. Compare the four negative feedback topologies,	
IV	Number Systems	09
	1. Introduction	
	2. Digital Representation of Analog Quantities,	
	3. Types of Number Systems: Binary Number System,	
	4. Octal Number System, Hexadecimal Number System,	
	5. Signed Binary Number Representation,	
	6. 1's Complement Representation,	
	7. 2's Complement Representation, 1011003 8. Binary Arithmetic: Binary Addition and Binary Subtraction,	
	9. Conversion of Numbers from One System to Another,	
	10. Numerical Problems.	
	Unit Outcome:	
	UO1. Represent numerical values in various number systems	

and	perform	binary	arithmetic	and	conversions	between	
diffe	rent numb	oer syste	ms				

Learning Resources: -

1. Basic Electronics (Solid-state) (Multicolor Illustrative Edition) B.L. Theraja. (S. Chand & Company Ltd)

2. Electric Fundamentals and Applications – John. D. Ryder (Prentice – Hall of India Pvt. Ltd.)

3. Modern Digital Electronics – R.P. Jain, Tata McGraw Hill Pub, Company (3rd edition)

4. Digital fundamental- Floyd (200<mark>5) Pear</mark>son Education

5. A text book of Applied Electronics- R. S. Sedha. (2008) S. Chand Publishing

6. Digital Electronics with practical Approach, G.N. Shinde, Shivani Publications (2003)

7. Digital Electronics Principle Device And Application, Anil Kumar, Wiley

8. Digital Logic and Computer Design Book by Morris Mano

9. Principal of Electronics, V.K. Me<mark>hta and Rohit Mehta, S.</mark> Chand and Company Ltd.

10. Basic Electronics, Bernard Grob, Tata Mc-Graw Hill Publications (2007) Tenth Edition.



27



(Autonomous) Faculty of Science and Technology Department of Physics and Electronics

Course Type: Lab Course Course Title: Lab Course –IV (Based on DSC-IV) Course Code: 101ELE1104 Credits: 01 Max. Marks: 50

Hours: 30

Learning Objectives

- LO1. To inculcate the idea of concep<mark>ts abou</mark>t operating point.
- LO2. To develop the knowledge abo<mark>ut types</mark> of biasing and its usefulness.
- LO3. To introduce small signal behavior of transistors.
- LO4. To familiarize the concepts of feedback amplifiers.
- LO5. To familiarize with different number systems and their applications.

Course outcomes

After completion of the cours<mark>e, s</mark>tudents will be able to-

- CO1. Demonstrate operation, biasing of small signal amplifiers.
- CO2. Represent numerical values in various number systems and perform binary arithmetic and conversions between different number systems.
- CO3. The working of transistor in three different regions (Cut off, Saturation and Active),
- CO4. How transistor is used for amplification of weak signals.
- CO5. To give understanding of various types of amplifier circuits.

Practical No.	Unit
1	Single stage CE amplifier (Frequency Response)
2	Single stage CC amplifier (Emitter follower)
3	Study of Transisto <mark>r amp</mark> lifier (Load, Line Analysis)
4	Study of transistor biasing and stability
5	Study of power amplifier.
6	Study of binary addition (BCD Adder)
7	Study of binary subtraction (BCD Subtractor)
8	Study of Binary to Decimal convertor
9	V-I characteristics CE NPN transistor

N.B.: Any Six Practical's from above.

Latur (Autonomous)



(Autonomous) Faculty of Science & Technology Department of Physics and Electronics

Course Type: VSC-II Course Title: Mobile Repairing Course Code: 101ELE2501 Credits: 02 Max. I

Max. Marks: 50

Lectures: 30 Hrs.

Learning Objectives:

LO1. To provide basic knowledge of Mobile Phones hardware.

LO2. To understand the various identifying issues, troubleshoot issues and techniques.

LO3. To learn Architecture of Mobile Phone.

Course Outcomes:

After successful completion of the course, students will be able to:

CO1. Find the problem and solution of various Mobile Phone Devices.

CO2. Start a mobile repair shop of their own.

CO3. Perform any issue-related tasks such as identifying issues, troubleshoot issues, repairing mobile phones, etc.

CO4. Get entry level (technician) jobs at relevant places, Mobile repair centers, mobile shops, mobile service centers

Unit No.	Title of Unit & Contents	Hrs.						
Ι	Introduction to Mobile	12						
	1 Information about Mobile							
	2 Invention of Mobile							
	3 Information about I-Phone							
	4 Invention of I Phone							
	5 Differenc <mark>e between</mark> Keyp <mark>ad mob</mark> ile And Smartphone mobile							
	Unit Outcome:							
	UO1. Find the problem and solution of various Mobile Phone							
	Devices.							
II	Information About IMEI number and tools for Mobile Repairing	10						
	1 Uses of IMEI number							
	2 Digit of IMEI number							
	3 Code of IMEI number							
	4 Information About all full forms							
	5. Tools for repair: Hot air gun, Shoulder gun, PCB Stand, multi-							
	meter, Kathli, Battery Booster, Screwdriver set, Tweezers, Battery Meter, Brush, Thinner, Paste, Jumper wire, Computer.							
	Unit Outcomes:							
	U01. Identify the IMEI number							
	Perform any issue-related tasks such as identifying issues,							
	troubleshoot issues, repairing mobile phones, etc.							
III	Practical	10						

1. Study of Mobile Phone Dictionary: Full Forms of Terms Used in	
Mobile Phone.	
2. Study of Mobile Phone Repairing Tools and Equipment.	
3. Identification of Card Level Parts.	
4. Identification of PCB.	
5. Identification of Small Parts in a Mobile Phone.	
6. Study About IC (Integrated Circuit) and Counting Techniques of	
Leg Type and Ball-Type IC.	
7. Software Problems and Solutions in mobile repairing.	
8. Multimedia and Downloading in Mobiles.	

Learning Resources:

1. Mobile Repairing Book in Hindi by Nitin Kothari

2. Mastering Mobile Learning by Chad Udell, Gary Woodill

3. Perfect Mobile Repairing Handbook: Smartphones Repairing Handbook Kindle Edition

4. Advance Mobile Repairing: Multicolour Circuits, Service Diagrams & Rep, Sanjib Pandit.

5. Mobile phone repair training course skills and research by MEI XIU JIANg.

6. Mobile Phones and Tablets Repairs: A Complete Guide for Beginners and Professionals, Chukky Oprandu





Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur (Autonomous) UG First Year

Basket IV: Generic/Open Elective (GE/OE)

(GEs offered to the Science & Technology students in Sem.-II)

Sr.	BoS Proposing	Code	Course Title	Credits	Hrs.
No.	GE/OE				
1	Commerce	101AAF <mark>2401</mark>	Mutual Fund	04	60
			Management		
2	Commerce	101MA <mark>E2401</mark>	Fundamentals of	04	60
			Statistics		
3	English	101EN <mark>G2402</mark>	English for Science and	04	60
			Technology		
4	Geography	101GE <mark>02401</mark>	General Geography	04	60
5	Commerce	101BAI <mark>2401</mark>	Personal Financial	04	60
			Management		
6	Marathi	101MAR2401	स्पर्धा परीक्षा आणि मराठी	04	60
			भाषा		
7	Political Science	101POL2401	Human Rights	04	60
8	Biotechnology	101BI02401	Nutrition, Health and	04	60
			Hygiene		
9	Music	101MUS2401	Indian Vocal Classical &	04	60
			Light Music		
10	NCC Studies	101NCC2401	Introduction to NCC	04	60
11	Sports	101 <mark>SPO2401</mark>	Counseling and	04	60
			Psychotherapy		
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Note: Student can choose any one GE from the basket.

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Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur (Autonomous) UG First Year

Basket V: Skill Enhancement Courses (SEC)

(SEC offered to the Science & Technology students in Sem.-II)

Sr. No.	BoS Proposing SEC	Code	Course Title	Credits	Hrs.
1	Chemistry	101CHE2601	Pesticides and Green Chemistry	02	30-45
2	Information Technology	101COM26 <mark>01</mark>	Basics of Python Programming	02	30-45
3	Physics	101PHY26 <mark>01</mark>	Physics Workshop Skills	02	30-45
4	Biotechnology	101BIO260 <mark>1</mark>	Food Processing Technology	02	30-45
5	Botany	101B0T2601	Mushroom Cultivation Technology	02	30-45
6	English	10 <mark>1EN</mark> G26 <mark>01</mark>	Proof Reading and Editing	02	30
7	Information Technology	1 <mark>01CO</mark> A2601	PC Assemble and Installation	02	30-45
8	Marathi	101MAR2601	कथा/पटकथालेखन	02	30
9	Zoology	101Z002601	Bee Keeping	02	30-45

Note: Student can choose any one SEC from the basket.

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Shiv Chhatrapati Shikshan Sanstha's Rajarshi Shahu Mahavidyalaya, Latur (Autonomous) UG First Year Sem II

Basket VI: Ability Enhancement Courses (AEC)

(AEC offered to the Science & Technology students in Sem.-II)

Sr.	BoS Proposing	Code	Course Title	Credits	Hrs.
No.	AEC				
1	Marathi	101MAR2701	लेलित वाङ्मय	02	30
2	Hindi	101HIN27 <mark>01</mark>	हिंदी भाषा शिक्षण एवं अनुवाद	02	30
3	Sanskrit	101SAN2 <mark>701</mark>	व्यावहारीक व्याकरण व	02	30
			<mark>निति</mark> सुभाषिते भाग – २		
4	Pali	101PAL27 <mark>01</mark>	निवडक गद्य व पद्य	02	30





(Autonomous) Faculty of Science and Technology Department of Physics and Electronics UG First Year

Extra Credit Activities

Sr.	Course Title	Credits	Hours
No.			T/P
1	MOOCs	Min. of 02 credits	Min. of 30 Hrs.
2	Certificate Courses	Min. of 02 credits	Min. of 30 Hrs.
3	IIT, Bombay	Min. of 02 credits	Min. of 30 Hrs.
	Spoken Tutorial		
	Courses		

Guidelines:

Extra -academic activities

- 1. All extra credits claimed under this heading will require sufficient academic input/ contribution from the students concerned.
- 2. Maximum 04 extra credits in each academic year will be allotted.
- 3. These extra academic activity credits will not be considered for calculation of SGPA/CGPA but will be indicated on the grade card.

Additional Credits for Online Courses:

- 1. Courses only from SWAYAM and NPTEL platform are eligible for claiming credits.
- 2. Students should get the consent from the concerned subject Teacher/Mentor/Vice Principal and Principal prior to starting of the course.
- 3. Students who complete such online courses for additional credits will be examined/verified by the concerned mentor/internal faculty member before awarding credits.
- 4. Credit allotted to the course by SWAYAM and NPTEL platform will be considered as it is.

Additional Credits for Other Academic Activities:

- 1. One credit for presentation and publication of paper in International/National/State level seminars/workshops.
- 2. One credit for measurable research work undertaken and field trips amounting to 30 hours of recorded work.
- 3. One credit for creating models in sponsored exhibitions/other exhibits, which are approved by the concerned department.
- 4. One credit for any voluntary social service/Nation building exercise which is in collaboration with the outreach center, equivalent to 30 hours
- 5. All these credits must be approved by the College Committee.

Additional Credits for Certificate Courses:

- 1. Students can get additional credits (number of credits will depend on the course duration) from certificate courses offered by the college.
- 2. The student must successfully complete the course. These credits must be approved by the Course Coordinators.
- 3. Students who undertake summer projects/ internships/ training in institutions of repute through a national selection process, will get 2 credits for each such activity. This must be done under the supervision of the concerned faculty/mentor.

Note:

- 1. The respective documents should be submitted within 10 days after completion of Semester End Examination.
- 2. No credits can be granted for organizing or for serving as office bearers/ volunteers for Inter-Class / Associations / Sports / Social Service activities.
- 3. The office bearers and volunteers may be given a letter of appreciation by the respective staff coordinators. Besides, no credits can be claimed for any services/activities conducted or attended within the college.
- 4. All claims for the credits by the students should be made and approved by the mentor in the same academic year of completing the activity.
- 5. Any grievances of denial/rejection of credits should be addressed to Additional Credits Coordinator in the same academic year.
- 6. Students having a shortage of additional credits at the end of the third year can meet the Additional Credits Coordinator, who will provide the right advice on the activities that can help them earn credits required for graduation.





(Autonomous) Faculty of Science and Technology Department of Physics and Electronics Examination Framework

Theory:

40% Continuous Assessment Tests (CATs) and 60% Semester End Examination (SEE) **Practical:**

50% Continuous Assessment Tests (CATs) and 50% Semester End Examination (SEE)

Course	Marks	CAT & Mid Term Theory				CAT Practical		Best Scored CAT & Mid Term	SEE	Total
				3		4				
1	2	Att.	CAT I	Mid Term	CAT II	Att.	САТ	5	6	5 + 6
DSC/DSE/ GE/OE/Minor	100	10	10	20	10	-	-	40	60	100
DSC	75	05	10	15	10	-	-	30	45	75
Lab Course/AIPC/ OJT/FP	50	-		-	-	05	20	-	25	50
VSC/SEC/ AEC/VEC/CC	50	05	05	10	05	-		20	30	50

Note:

- 1. All Internal Exams are compulsory
- 2. Out of 02 CATs best score will be considered
- 3. Mid Term Exam will be conducted by the Exam Section
- 4. Mid Term Exam is of Objective nature (MCQ)
- 5. Semester End Exam is of descriptive in nature (Long & Short Answer)
- 6. CAT Practical (20 Marks): Lab Journal (Record Book) 10 Marks, Overall Performance 10 Marks.

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